	Type	L #	Hits	Search Text	DBs
1	BRS	L1	219	712/35.ccls.	USPAT
2	BRS	L2	452	712/228.ccls.	USPAT
3	BRS	L3	2830	711/147-153.ccls.	USPAT
4	BRS	L4	928	710/5.ccls.	USPAT
5	BRS	L12	76988	MAC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
6	BRS	L13	633	MAC adj unit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
7	BRS	L14	385	MAC adj processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
8	BRS	L15	13	morton.in. and FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
9	BRS	L16	10497	input adj processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
10	BRS	L19	1871	L16 and (output adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
11	BRS	L20	66	L19 and (master adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
12	BRS	L21	48	L20 and dedicated	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
13	BRS	L24	1	L20 and (dedicated adj processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
14	BRS	L25	30	l20 and (instruction adj set)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
15	BRS	L26	0	l20 and (different adj instruction adj set)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB